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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10033397	FILING DATE 12/27/2001	CLASS 257	SUBCLASS 109	GAU 2815	EXAMINER Mary Wilc2ewski
**APPLICANTS: Yatsuda Hiromi;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/583,382 05/30/2000					
BEST AVAILABLE COPY					
** FOREIGN APPLICATIONS VERIFIED: JAPAN HEI 11-155242 06/02/1999 JAPAN 2000-137130 05/10/2000					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiner's initials				ATTORNEY DOCKET NO 5622DIV	
TITLE : Electronic component to be mounted on a circuit board having electronic circuit device sealed therein and method of manufacturing the same					
<small>U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	
		Print Claim for O.G.	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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